

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

J1050 U.S. PTO
10/016793
10/26/01

**INFORMATION DISCLOSURE
STATEMENT**

Docket Number
2207/12116

Application Number
To Be Assigned

Filing Date
Herewith

Examiner
To Be Assigned

Art Unit
To Be Assigned

Invention Title
SILICON INTERPOSER-BASED HYBRID
VOLTAGE REGULATOR SYSTEM FOR VLSI
DEVICES

Inventor(s)
NAIR et al.

Assistant Commissioner
for Patents
Washington, D.C. 20231
Box Patent Application

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. The references are listed on the attached modified PTO form 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 10/26/01

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INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO FORM 1449	Atty. Docket No. 2207/12116	Serial No. To Be Assigned
	Applicant(s) NAIR et al.	
	Filing Date Herewith	Group To Be Assigned

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U. S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,955,870	Sep. 21, 1999	Nair			
	6,081,105	June 27, 2000	Nair			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER'S INITIALS		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	